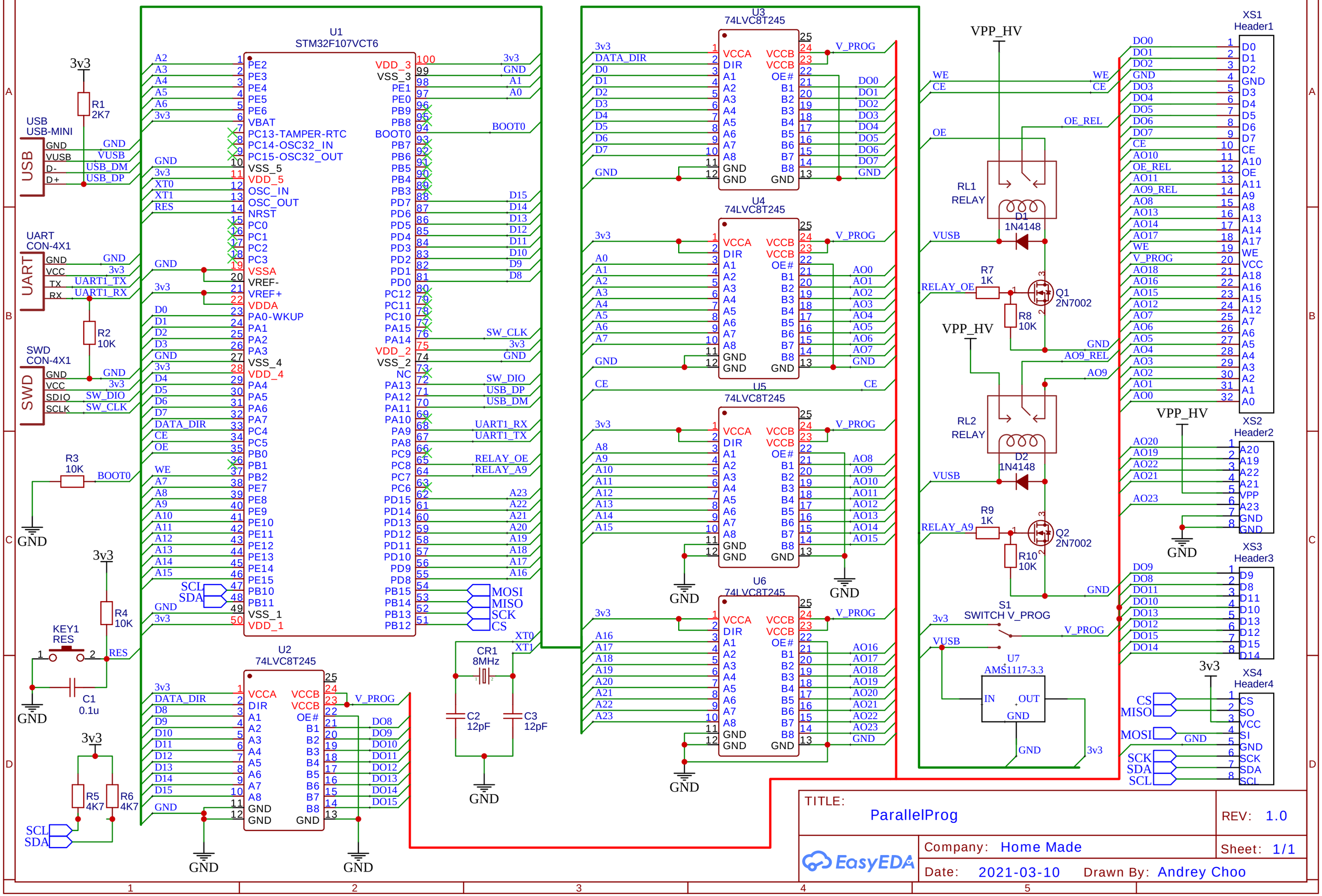
U1
STM32F107VCT6
PE2
PE3
PE4
PE5
PE6
VBAT
PC13-TAMPER-RTC
PC14-OSC32_IN
PC15-OSC32_OUT
VSS_5
VDD_5
OSC_IN
OSC_OUT
NRST
PC0
PC1
PC2
PC3
VSSA
VREF-
VREF+
VDDA
PA0-WKUP
PA1
PA2
PA3
VSS_4
VDD_4
PA4
PA5
PA6
PA7
PC4
PC5
PB0
PB1
PB2
PE7
PE8
PE9
PE10
PE11
PE12
PE13
PE14
PE15
PB10
PB11
VSS_1
VDD_1
VDD_3
VSS_3
PE1
PE0
PB9
PB8
BOOT0
PB7
PB6
PB5
PB4
PB3
PD7
PD6
PD5
PD4
PD3
PD2
PD1
PD0
PC12
PC11
PC10
PA15
PA14
VDD_2
VSS_2
NC
PA13
PA12
PA11
PA10
PA9
PA8
PC9
PC8
PC7
PC6
PD15
PD14
PD13
PD12
PD11
PD10
PD9
PD8
PB15
PB14
PB13
PB12
USB
USB-MINI
R1
2K7
UART
CON-4X1
R2
10K
SWD
CON-4X1
R3
10K
R4
10K
KEY1
RES
C1
0.1u
R5
4K7
R6
4K7
U2
74LVC8T245
CR1
8MHz
C2
12pF
C3
12pF
U3
74LVC8T245
U4
74LVC8T245
U5
74LVC8T245
U6
74LVC8T245
V_PROG
RL1
RELAY
D1
1N4148
R7
1K
Q1
2N7002
R8
10K
RL2
RELAY
D2
1N4148
R9
1K
Q2
2N7002
R10
10K
S1
SWITCH V_PROG
U7
AMS1117-3.3
XS1
Header1
XS2
Header2
XS3
Header3
XS4
Header4
TITLE:
ParallelProg
REV: 1.0
Company: Home Made
Sheet: 1/1
Date: 2021-03-10
Drawn By: Andrey Choo
EasyEDA